

FIG. 1

214 210

230 250

3 digital logic circuit A/D converter

Output

FIG. 2A

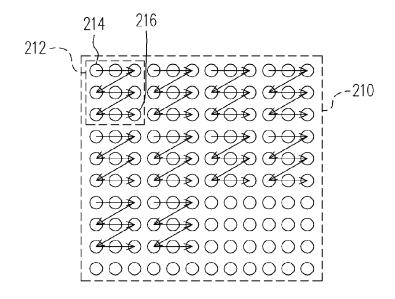


FIG. 2B

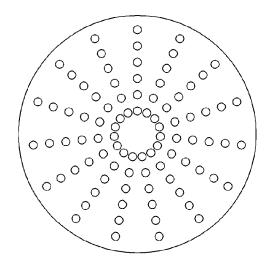


FIG. 2C

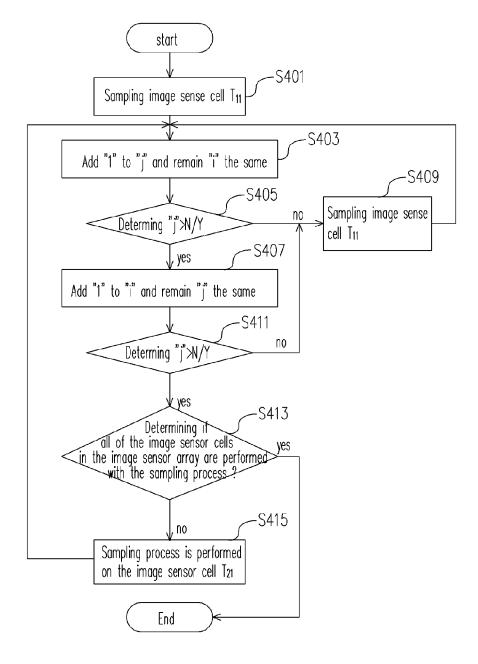


FIG. 3

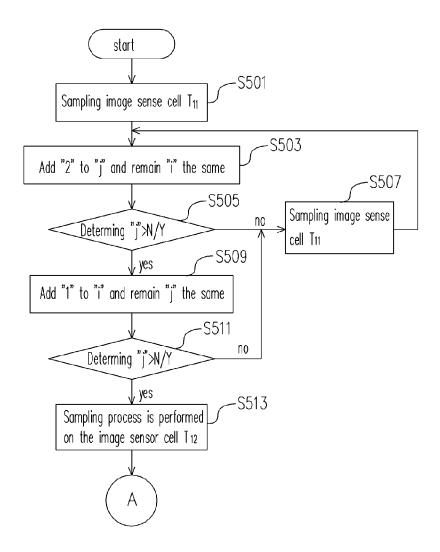


FIG. 4A

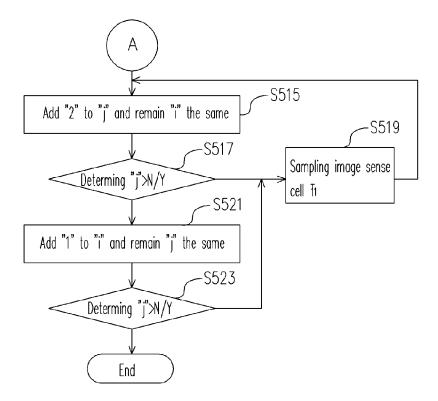


FIG. 4B